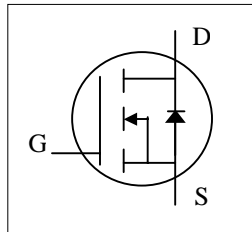
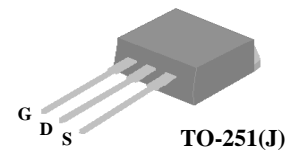




- ▼ 100% Avalanche Test
- ▼ Fast Switching Characteristics
- ▼ Simple Drive Requirement
- ▼ RoHS Compliant & Halogen-Free



BV_{DSS}	600V
$R_{DS(ON)}$	8Ω
I_D	1.6A



Description

AP01N60 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The straight lead version TO-251 package is widely preferred for all commercial-industrial through hole applications.

Absolute Maximum Ratings @ $T_J=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	600	V
V_{GS}	Gate-Source Voltage	± 30	V
$I_D @ T_C=25^\circ\text{C}$	Drain Current, $V_{GS} @ 10\text{V}$	1.6	A
$I_D @ T_C=100^\circ\text{C}$	Drain Current, $V_{GS} @ 10\text{V}$	1	A
I_{DM}	Pulsed Drain Current ¹	6	A
$P_D @ T_C=25^\circ\text{C}$	Total Power Dissipation	39	W
E_{AS}	Single Pulse Avalanche Energy ²	13	mJ
I_{AR}	Avalanche Current	1.6	A
E_{AR}	Repetitive Avalanche Energy	0.5	mJ
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	3.2	$^\circ\text{C}/\text{W}$
Rthj-a	Maximum Thermal Resistance, Junction-ambient	110	$^\circ\text{C}/\text{W}$



AP01N60J-HF

Electrical Characteristics @T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	600	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ³	V _{GS} =10V, I _D =0.8A	-	-	8	Ω
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2	-	4	V
g _{fs}	Forward Transconductance	V _{DS} =50V, I _D =0.8A	-	0.8	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =600V, V _{GS} =0V	-	-	10	uA
	Drain-Source Leakage Current (T _j =125°C)	V _{DS} =480V, V _{GS} =0V	-	-	100	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
Q _g	Total Gate Charge ³	I _D =1.6A	-	7.7	-	nC
Q _{gs}	Gate-Source Charge	V _{DS} =480V	-	1.5	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =10V	-	2.6	-	nC
t _{d(on)}	Turn-on Delay Time ³	V _{DD} =300V	-	8	-	ns
t _r	Rise Time	I _D =1.6A	-	5	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =10Ω	-	14	-	ns
t _f	Fall Time	V _{GS} =10V	-	7	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	286	-	pF
C _{oss}	Output Capacitance	V _{DS} =25V	-	25	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	5	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
I _S	Continuous Source Current (Body Diode)	V _D =V _G =0V , V _S =1.5V	-	-	1.6	A
I _{SM}	Pulsed Source Current (Body Diode) ¹		-	-	6	A
V _{SD}	Forward On Voltage ³	T _j =25°C, I _S =1.6A, V _{GS} =0V	-	-	1.5	V

Notes:

- 1.Pulse width limited by max. junction.
- 2.Starting T_j=25°C , V_{DD}=50V , L=10mH , R_G=25Ω , I_{AS}=1.6A.
- 3.Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

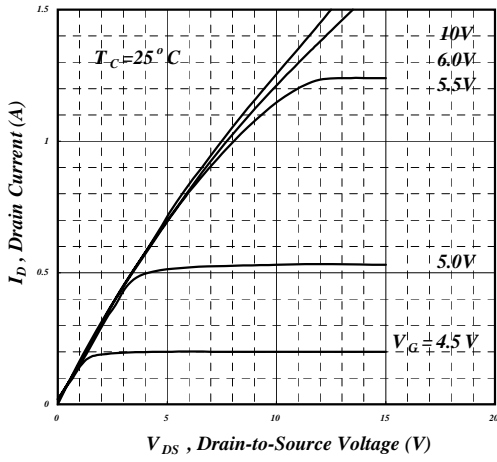


Fig 1. Typical Output Characteristics

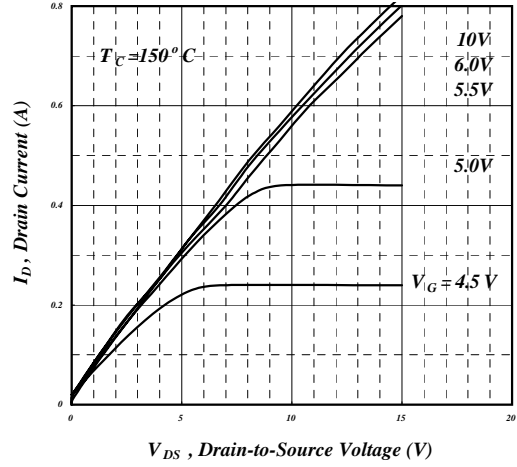


Fig 2. Typical Output Characteristics

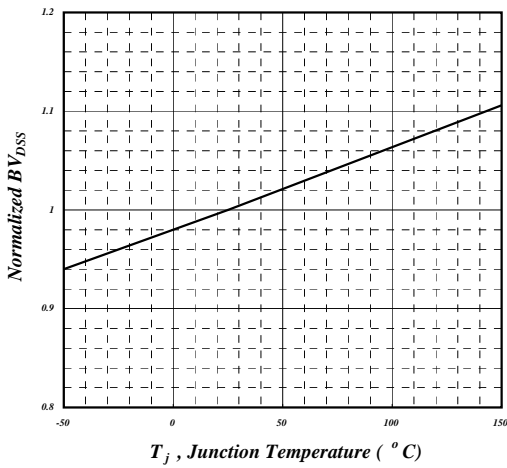


Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

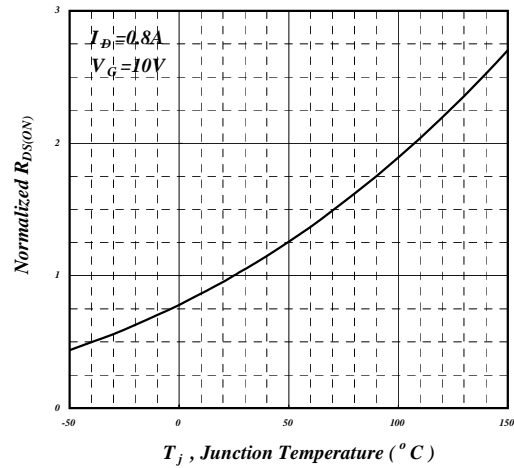


Fig 4. Normalized On-Resistance v.s. Junction Temperature

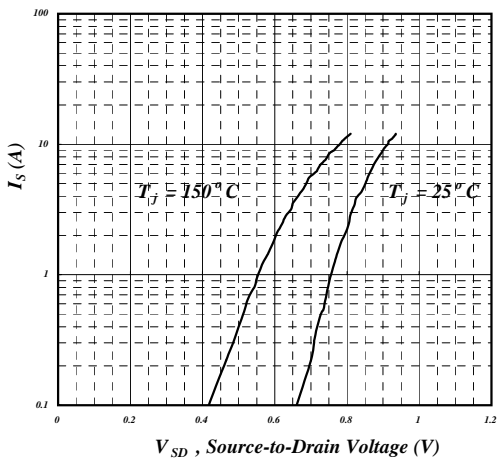


Fig 5. Forward Characteristic of Reverse Diode

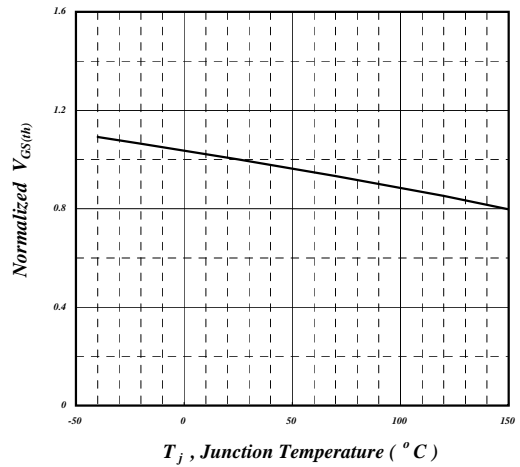


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

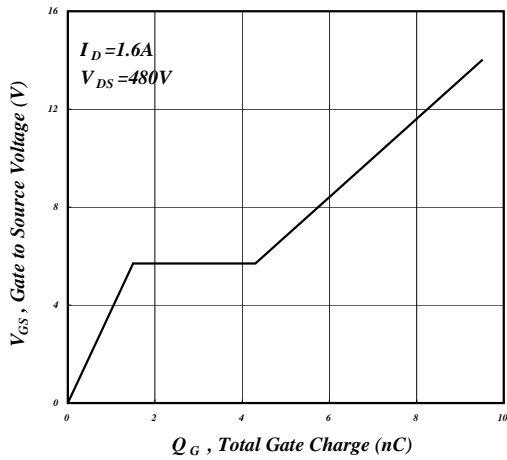


Fig 7. Gate Charge Characteristics

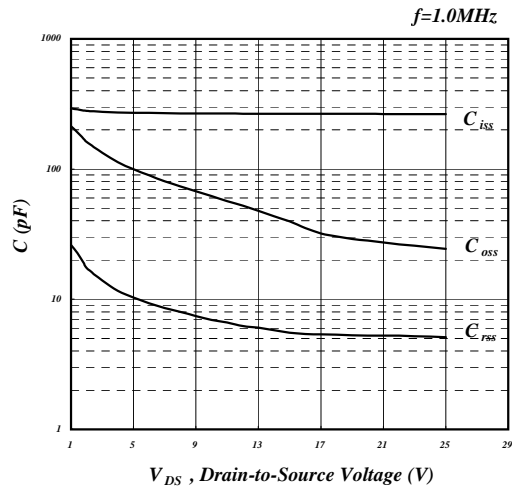


Fig 8. Typical Capacitance Characteristics

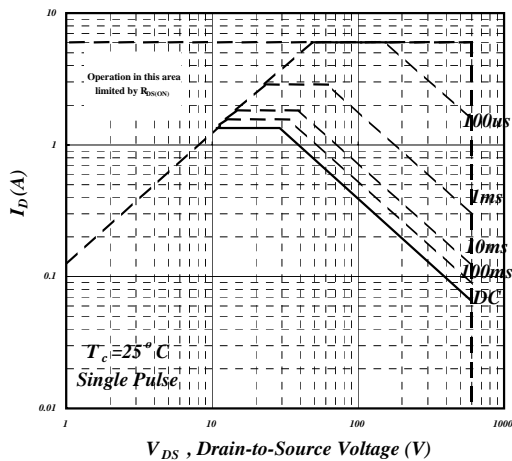


Fig 9. Maximum Safe Operating Area

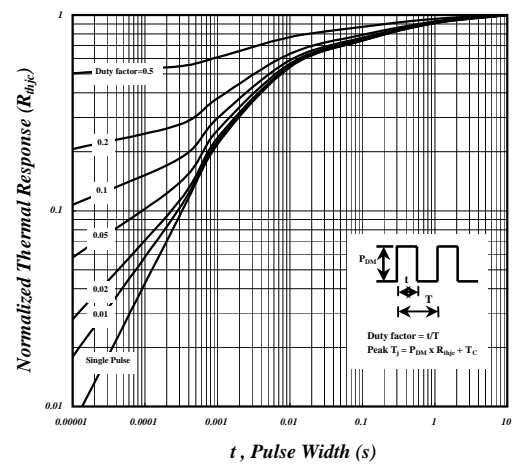


Fig 10. Effective Transient Thermal Impedance



Fig 11. Switching Time Waveform

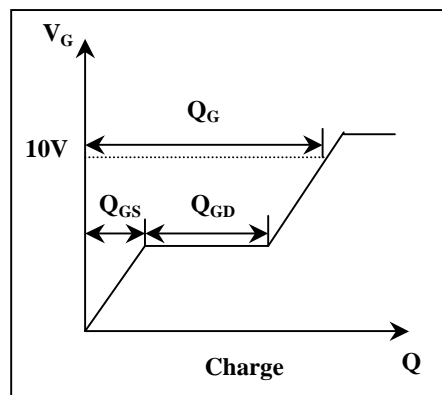
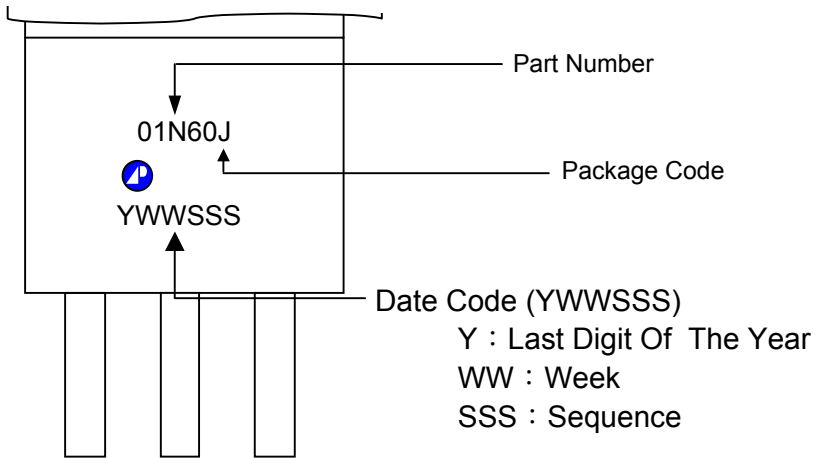


Fig 12. Gate Charge Waveform

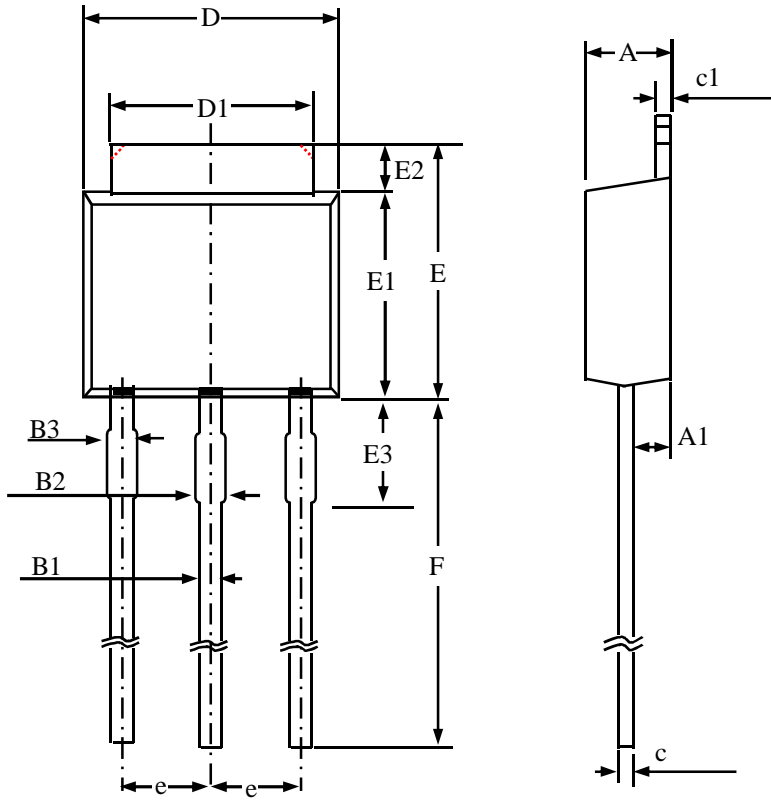


MARKING INFORMATION





Package Outline : TO-251



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	2.10	2.30	2.50
A1	0.80	1.15	1.50
B1	0.40	0.70	1.00
B2	0.60	0.88	1.15
B3	0.50	0.83	1.15
c	0.30	0.50	0.70
c1	0.30	0.50	0.70
D	6.30	6.55	6.80
D1	4.80	5.20	5.60
E	6.70	7.10	7.50
E1	5.30	5.80	6.30
E2	0.50	1.10	1.70
E3	1.30	1.80	2.30
e	----	2.30	----
F	7.00	8.33	9.65

- 1.All Dimensions Are in Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.



TO-251 FOOTPRINT :

