



**Advanced Power
Electronics Corp.**

AP10A250MT

Halogen-Free Product

DUAL N-CHANNEL ENHANCEMENT

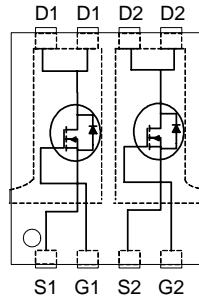
MODE POWER MOSFET

- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant & Halogen-Free

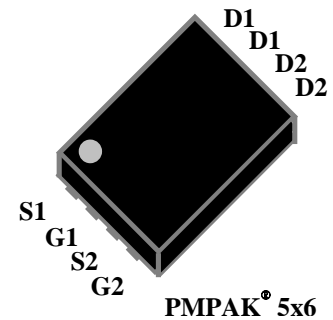
Description

AP10A250 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device fo

PMPAK[®] 5x6 dual pad provide superior thermal performance and is design for surface mount applications.



BV_{DSS}	100V
$R_{DS(ON)}$	250m Ω



Absolute Maximum Ratings @ $T_J=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C=25^\circ\text{C}$	Drain Current, $V_{GS} @ 10\text{V}$	4.7	A
$I_D @ T_A=25^\circ\text{C}$	Drain Current ³ , $V_{GS} @ 10\text{V}$	2.5	A
$I_D @ T_A=70^\circ\text{C}$	Drain Current ³ , $V_{GS} @ 10\text{V}$	2	A
I_{DM}	Pulsed Drain Current ¹	10	A
$P_D @ T_A=25^\circ\text{C}$	Total Power Dissipation	3.57	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Rating	Units
R_{thj-c}	Maximum Thermal Resistance, Junction-case	10	$^\circ\text{C}/\text{W}$
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient ³	35	$^\circ\text{C}/\text{W}$



AP10A250MT

Electrical Characteristics @T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	100	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =2A	-	-	250	mΩ
		V _{GS} =4.5V, I _D =1A	-	-	500	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	1	-	3	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =2A	-	3	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V	-	-	25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Q _g	Total Gate Charge	I _D =2A	-	5.5	8.8	nC
Q _{gs}	Gate-Source Charge	V _{DS} =50V	-	1.1	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =10V	-	1.8	-	nC
t _{d(on)}	Turn-on Delay Time	V _{DS} =50V	-	6	-	ns
t _r	Rise Time	I _D =1A	-	7	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω	-	10	-	ns
t _f	Fall Time	V _{GS} =10V	-	6	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	200	320	pF
C _{oss}	Output Capacitance	V _{DS} =50V	-	25	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	20	-	pF
R _g	Gate Resistance	f=1.0MHz	-	2	4	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =2.9A, V _{GS} =0V	-	-	1.3	V
t _{rr}	Reverse Recovery Time	I _S =2A, V _{GS} =0V	-	30	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	24	-	nC

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in² copper pad of FR4 board, t ≤10sec ; 85 °C/W on steady state.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

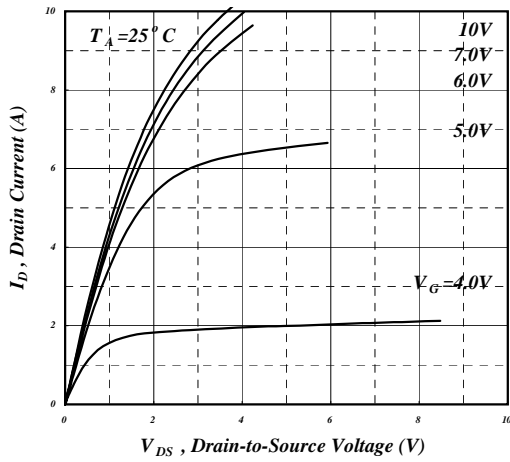


Fig 1. Typical Output Characteristics

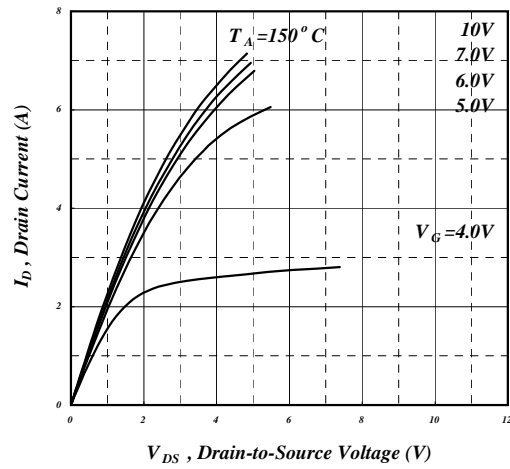


Fig 2. Typical Output Characteristics

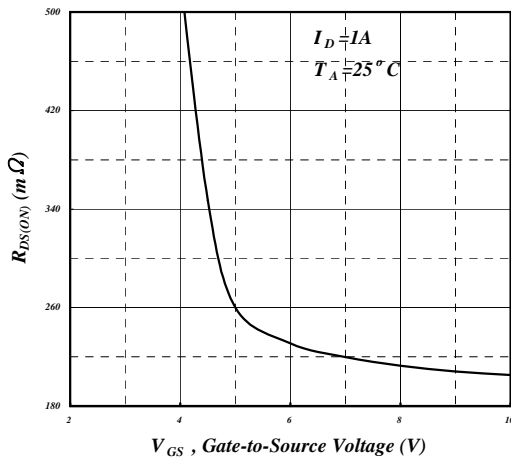


Fig 3. On-Resistance v.s. Gate Voltage

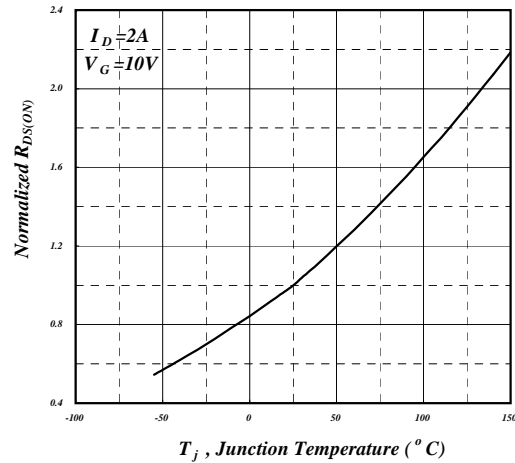


Fig 4. Normalized On-Resistance v.s. Junction Temperature

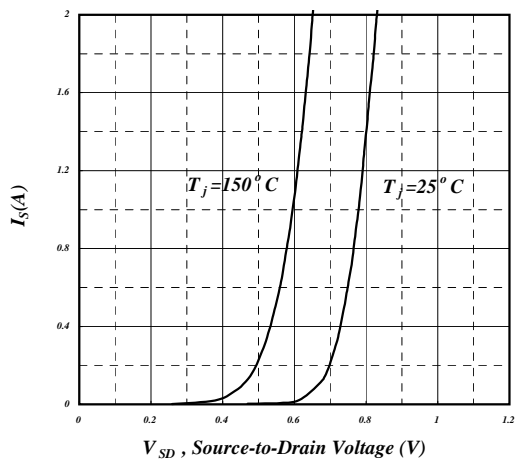


Fig 5. Forward Characteristic of Reverse Diode

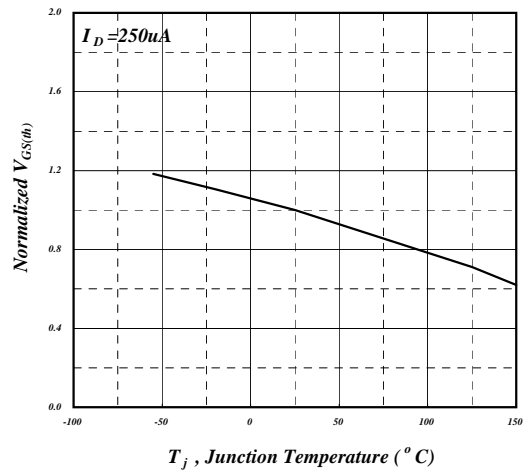


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

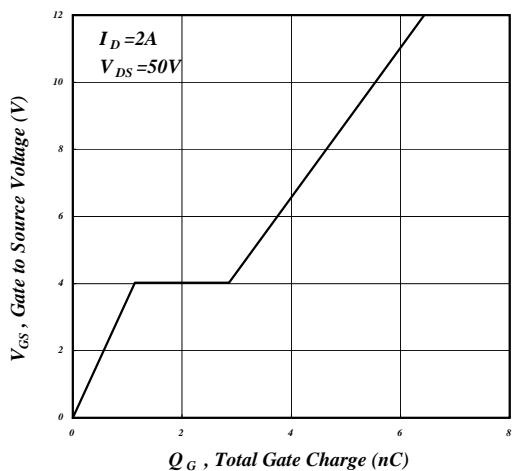


Fig 7. Gate Charge Characteristics

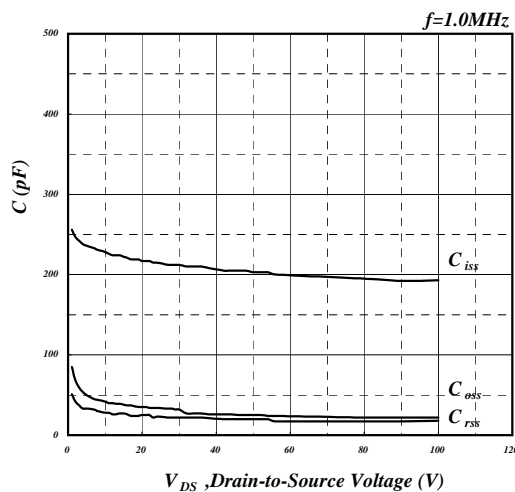


Fig 8. Typical Capacitance Characteristics

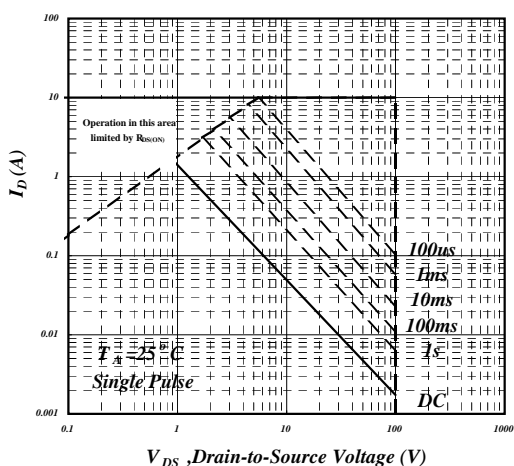


Fig 9. Maximum Safe Operating Area

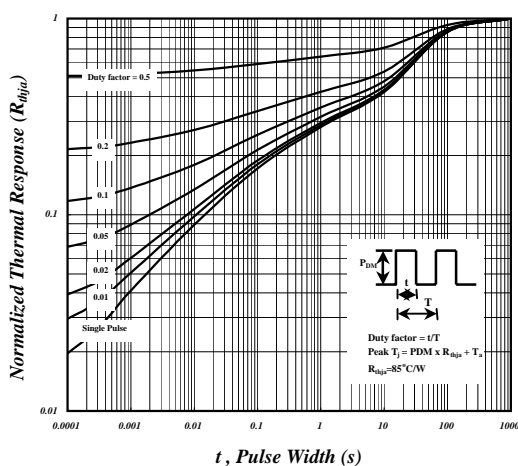


Fig 10. Effective Transient Thermal Impedance

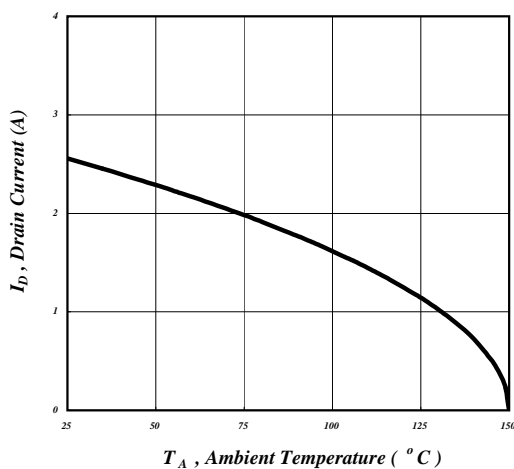


Fig 11. Drain Current v.s. Ambient Temperature

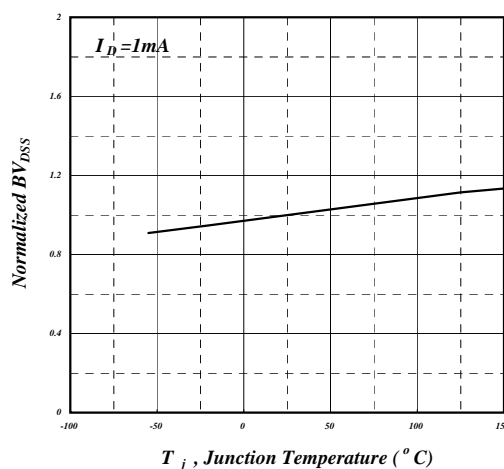
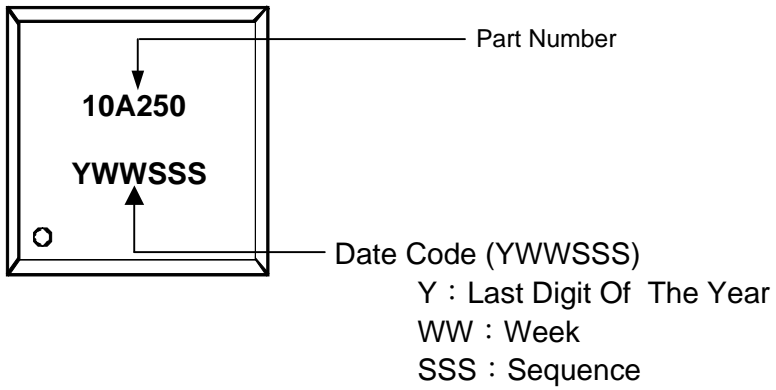
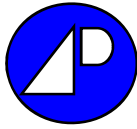


Fig 12. Normalized BV_{DSS} v.s. Junction Temperature

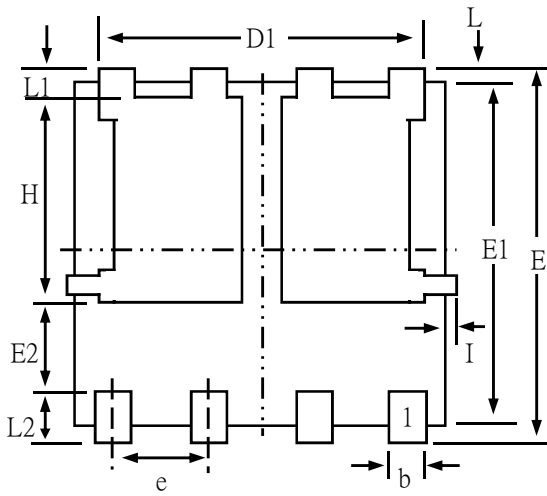


MARKING INFORMATION

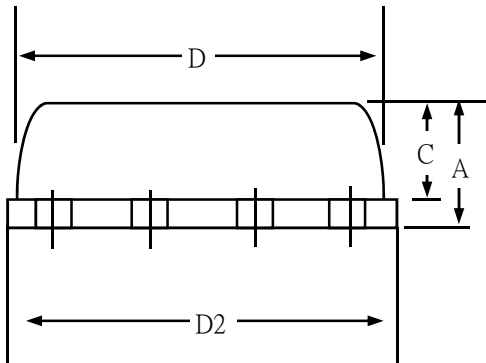




Package Outline : PMPAK 5x6 (Dual Pad)



BACKSIDE VIEW



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	0.80	1.00	1.20
b	0.34	0.42	0.50
C	0.54	0.76	0.97
D	4.80	4.95	5.10
D1	4.11	4.21	4.31
E	5.90	6.05	6.20
E1	5.60	5.75	5.90
E2	1.60 (ref.)		
e	1.27 (ref.)		
L	0.05	0.15	0.25
L1	0.60 (ref.)		
L2	0.60 (ref.)		
H	3.60 (ref.)		
I	0.15 (ref.)		
D2	4.80	5.15	5.50

1.All Dimension Are In Millimeters.

2.Dimension Does Not Include Mold Protrusions.



PMPAK5X6(Dual Pad,左右) FOOTPRINT :

