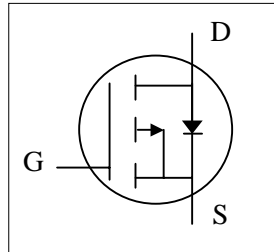




- ▼ Lower Gate Charge
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant & Halogen-Free

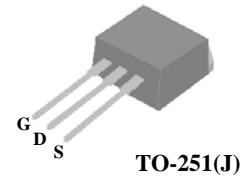
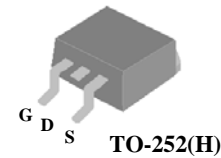


$BV_{DSS}$	-100V
$R_{DS(ON)}$	140m $\Omega$
$I_D$	-12A

### Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-252 package is widely preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters. The through-hole version (AP18P10AGJ) is available for low-profile applications.



### Absolute Maximum Ratings @ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-100	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D @ T_C=25^\circ\text{C}$	Drain Current, $V_{GS} @ 10\text{V}$	-12	A
$I_D @ T_C=100^\circ\text{C}$	Drain Current, $V_{GS} @ 10\text{V}$	-7.4	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	-48	A
$P_D @ T_C=25^\circ\text{C}$	Total Power Dissipation	39	W
$P_D @ T_A=25^\circ\text{C}$	Total Power Dissipation <sup>3</sup>	2	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

### Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	3.2	$^\circ\text{C}/\text{W}$
Rthj-a	Maximum Thermal Resistance, Junction-ambient (PCB mount) <sup>3</sup>	62.5	$^\circ\text{C}/\text{W}$
Rthj-a	Maximum Thermal Resistance, Junction-ambient	110	$^\circ\text{C}/\text{W}$



# AP18P10AGH/J-HF

## Electrical Characteristics @T<sub>j</sub>=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-100	-	-	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-8A	-	-	140	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-2	-	-4	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> = -10V, I <sub>D</sub> = -8A	-	13	-	S
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-80V, V <sub>GS</sub> =0V	-	-	-25	uA
I <sub>GSS</sub>	Gate-Source Leakage	V <sub>GS</sub> = +30V, V <sub>DS</sub> =0V	-	-	±100	nA
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> =-8A	-	35	55	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =-80V	-	4.5	-	nC
Q <sub>gd</sub>	Gate-Drain ("Miller") Charge	V <sub>GS</sub> =-10V	-	14.5	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DS</sub> =-50V	-	9	-	ns
t <sub>r</sub>	Rise Time	I <sub>D</sub> =-8A	-	16	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time	R <sub>G</sub> =3.3Ω	-	42	-	ns
t <sub>f</sub>	Fall Time	V <sub>GS</sub> =-10V	-	34	-	ns
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V	-	1380	2200	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =-25V	-	115	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f=1.0MHz	-	95	-	pF
R <sub>g</sub>	Gate Resistance	f=1.0MHz	-	7	14	Ω

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V <sub>SD</sub>	Forward On Voltage <sup>2</sup>	I <sub>S</sub> =-8A, V <sub>GS</sub> =0V	-	-	-1.3	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> =-8A, V <sub>GS</sub> =0V,	-	55	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=-100A/μs	-	155	-	nC

### Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

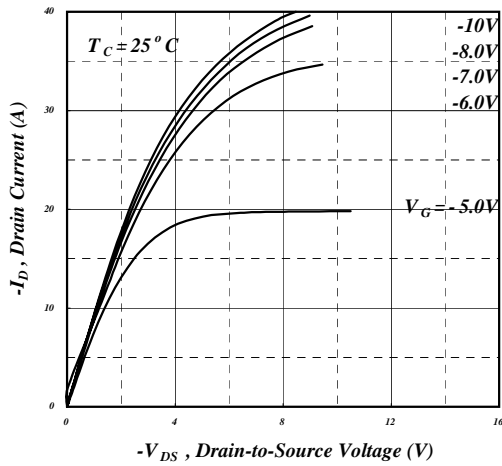


Fig 1. Typical Output Characteristics

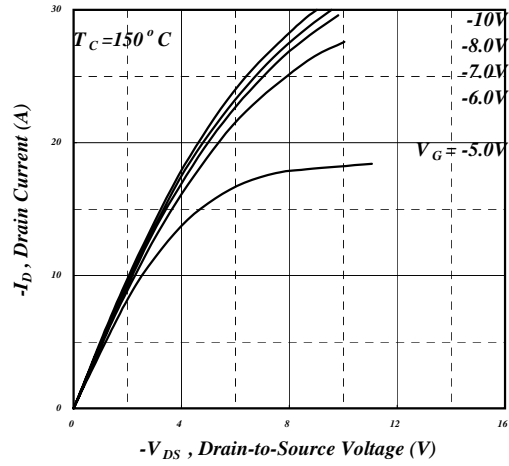


Fig 2. Typical Output Characteristics

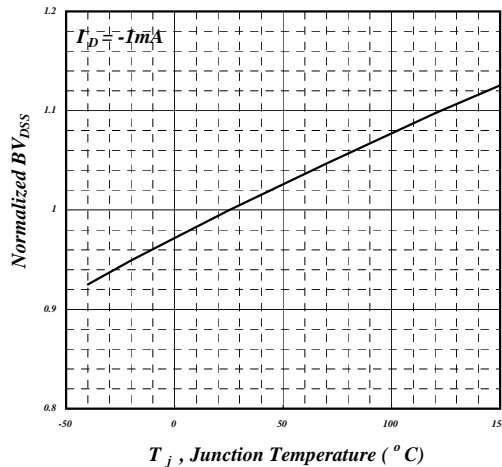


Fig 3. Normalized  $BV_{DS}$  v.s. Junction Temperature

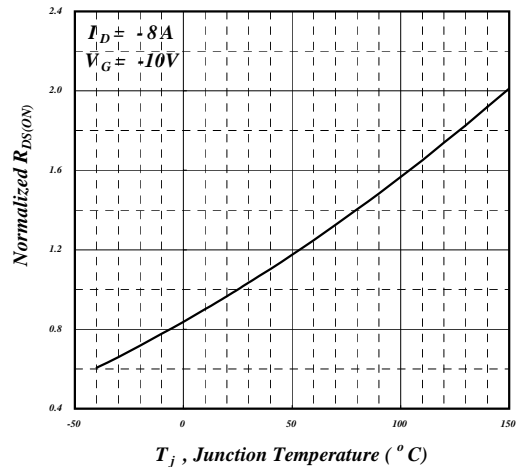


Fig 4. Normalized On-Resistance v.s. Junction Temperature

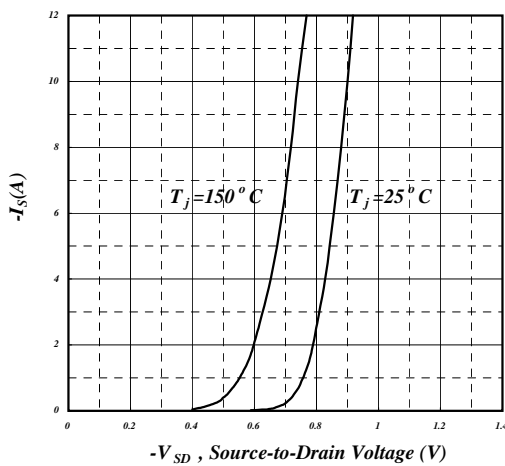


Fig 5. Forward Characteristic of Reverse Diode

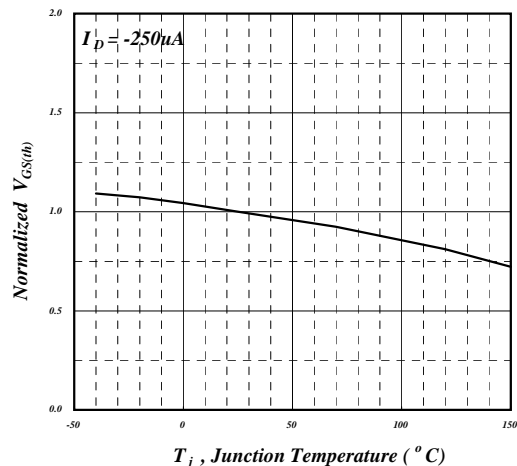


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

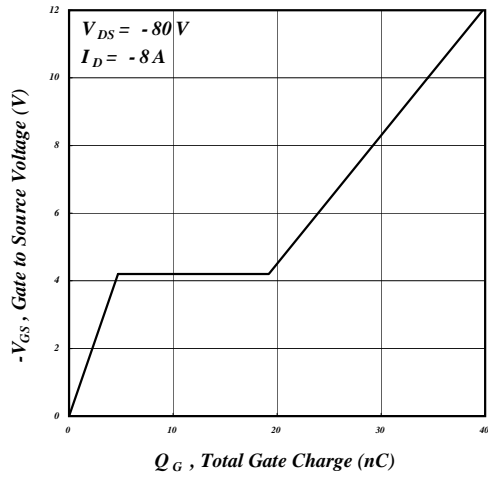


Fig 7. Gate Charge Characteristics

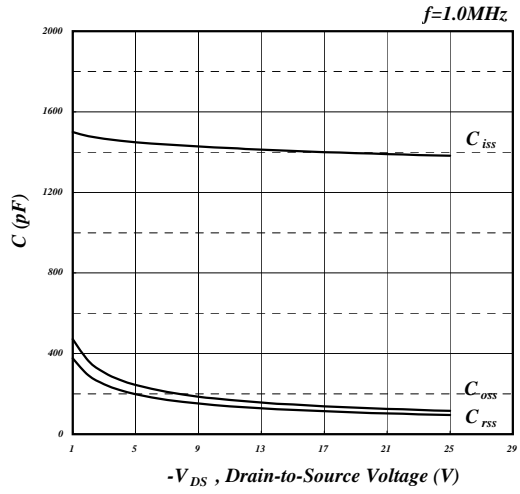


Fig 8. Typical Capacitance Characteristics

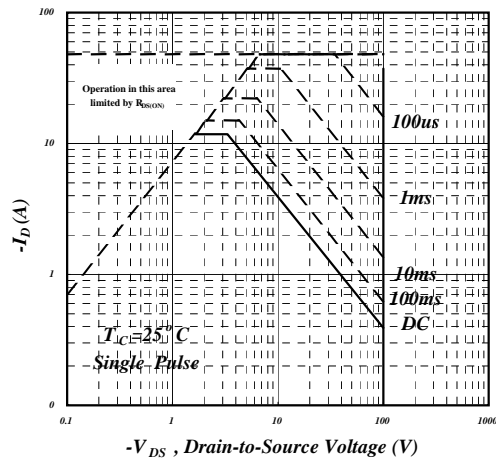


Fig 9. Maximum Safe Operating Area

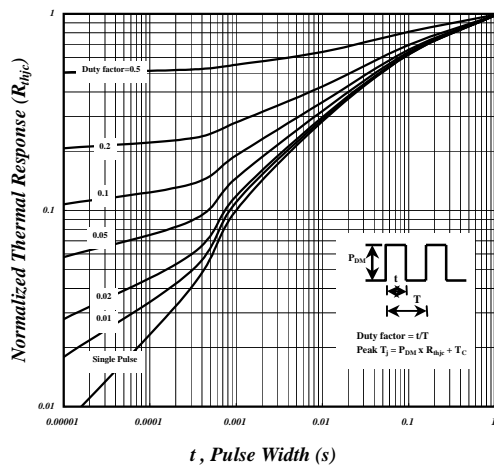


Fig 10. Effective Transient Thermal Impedance

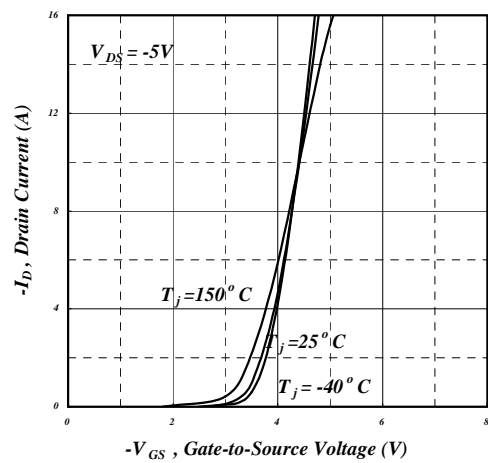


Fig 11. Transfer Characteristics

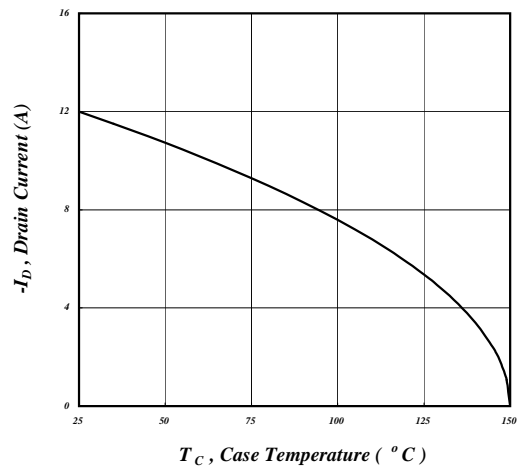
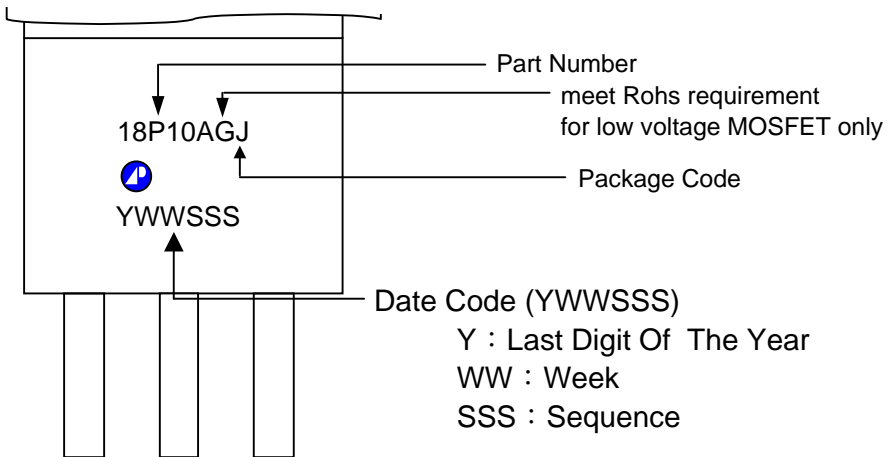


Fig 12. Drain Current v.s. Case Temperature

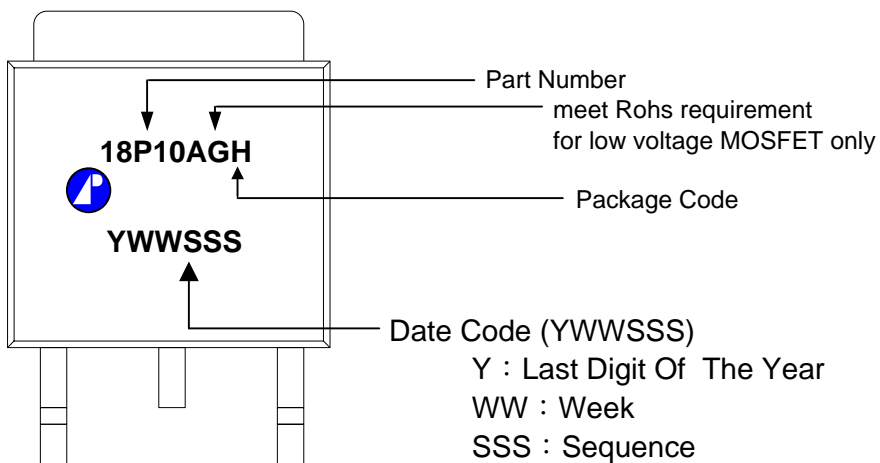


# MARKING INFORMATION

## TO-251

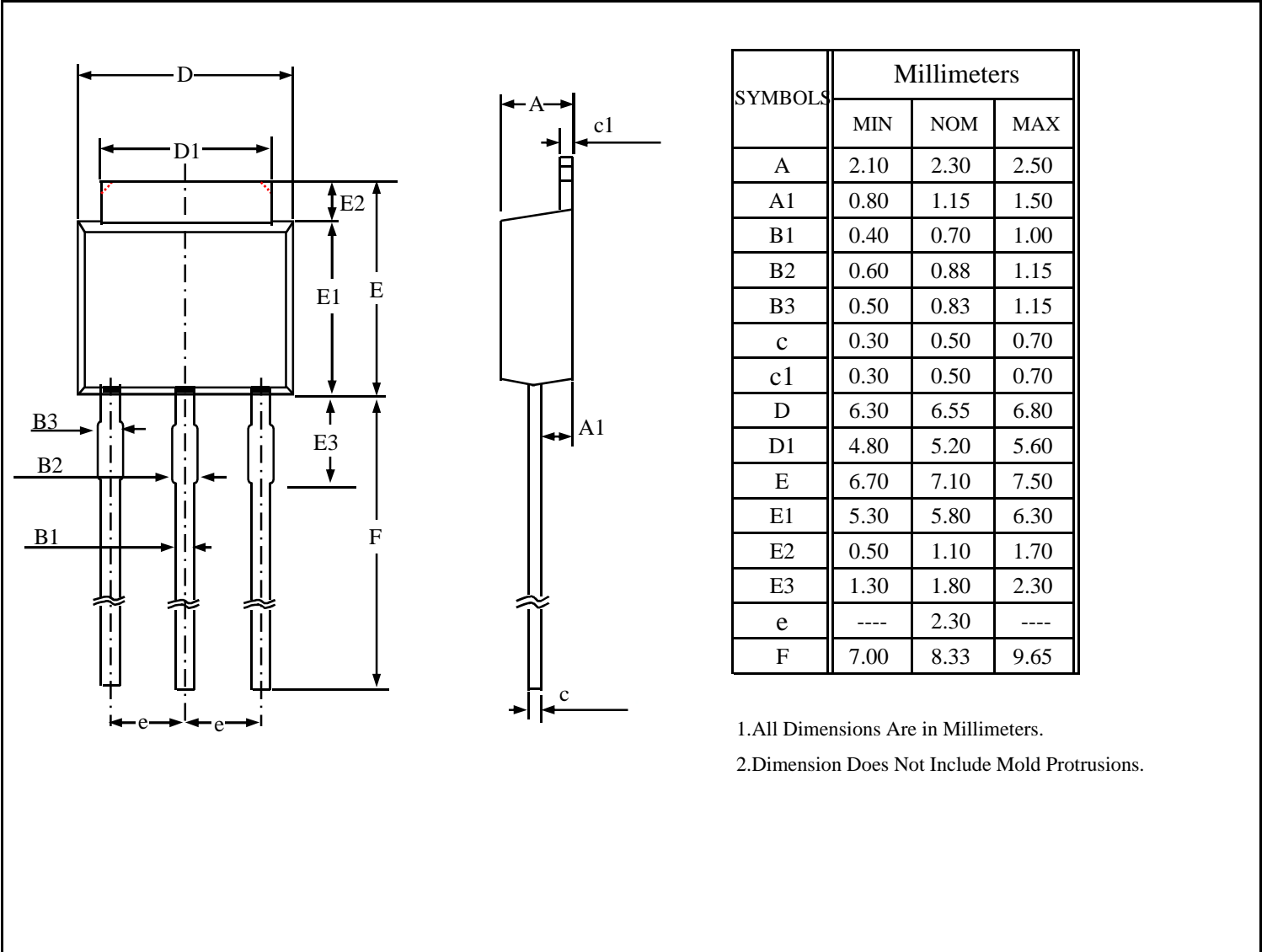


## TO-252





## Package Outline : TO-251



- 1.All Dimensions Are in Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.



**TO-251 FOOTPRINT :**

